

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):	Mattias Nilsson, et al.	§	Group Art Unit:	Unknown
		§		
Serial No:	Unknown	§	Examiner:	Unknown
		§		
Filed:	January 5, 2006	§		
		§		

For: Testing of Interconnections Between Stacked Circuit Boards

Via EFS-Web

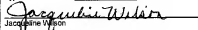
Mail Stop PCT
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF TRANSMISSION BY EFS-WEB

Date of Transmission: January 5, 2006

I hereby certify that this paper or fee is being transmitted to the United States Patent and Trademark Office electronically via EFS-Web.

Type or Print Name: Jacqueline Wilson


Jacqueline Wilson

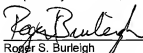
Dear Sir:

INFORMATION DISCLOSURE STATEMENT

In accordance with the duty of disclosure as set forth in 37 C.F.R. § 1.56, Applicant(s) hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Copies of the cited references were previously transmitted to the USPTO by the international searching authority.

To assist the Examiner, the documents are listed on the attached form PTO 1449. It is respectfully requested that the Examiner initial a copy of this form and return it to the undersigned.

Respectfully submitted,



Roger S. Burleigh
Reg. No. 40,542

Ericsson Inc.
6300 Legacy Drive
M/S EVR 1-C-11
Plano, TX 75024
972-583-5799
roger.burleigh@ericsson.com

